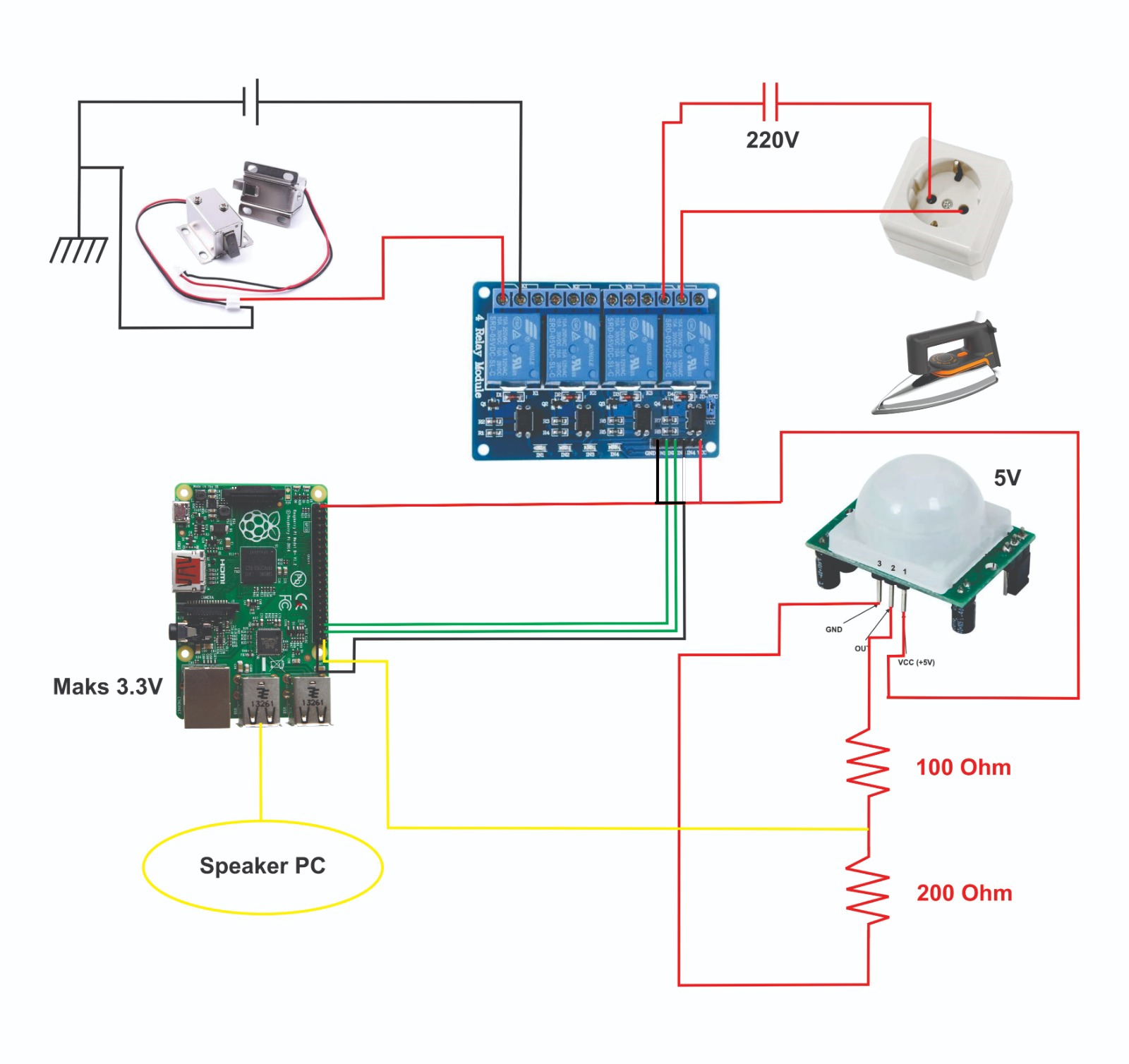
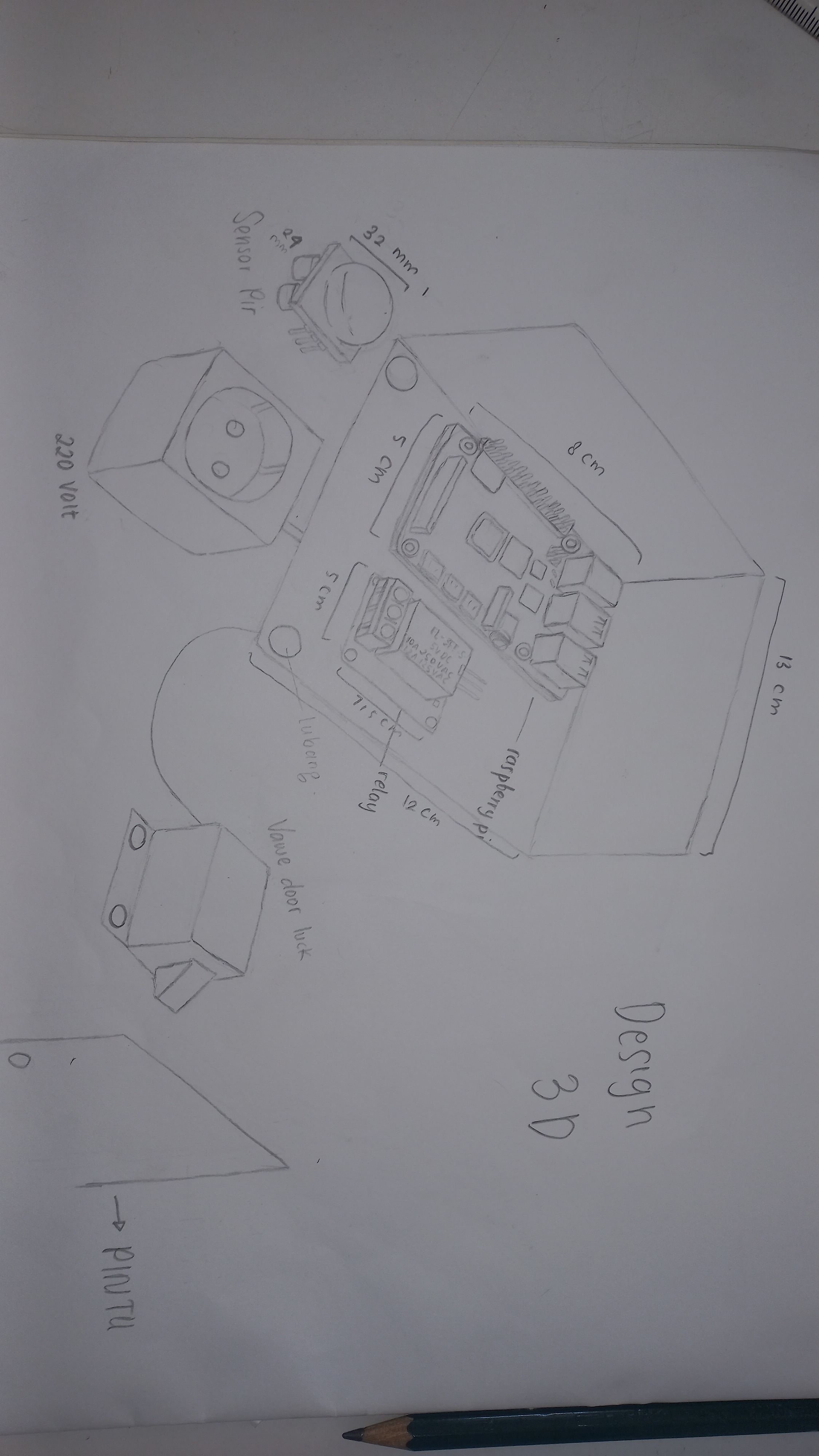
Technical Assignment Kel 59

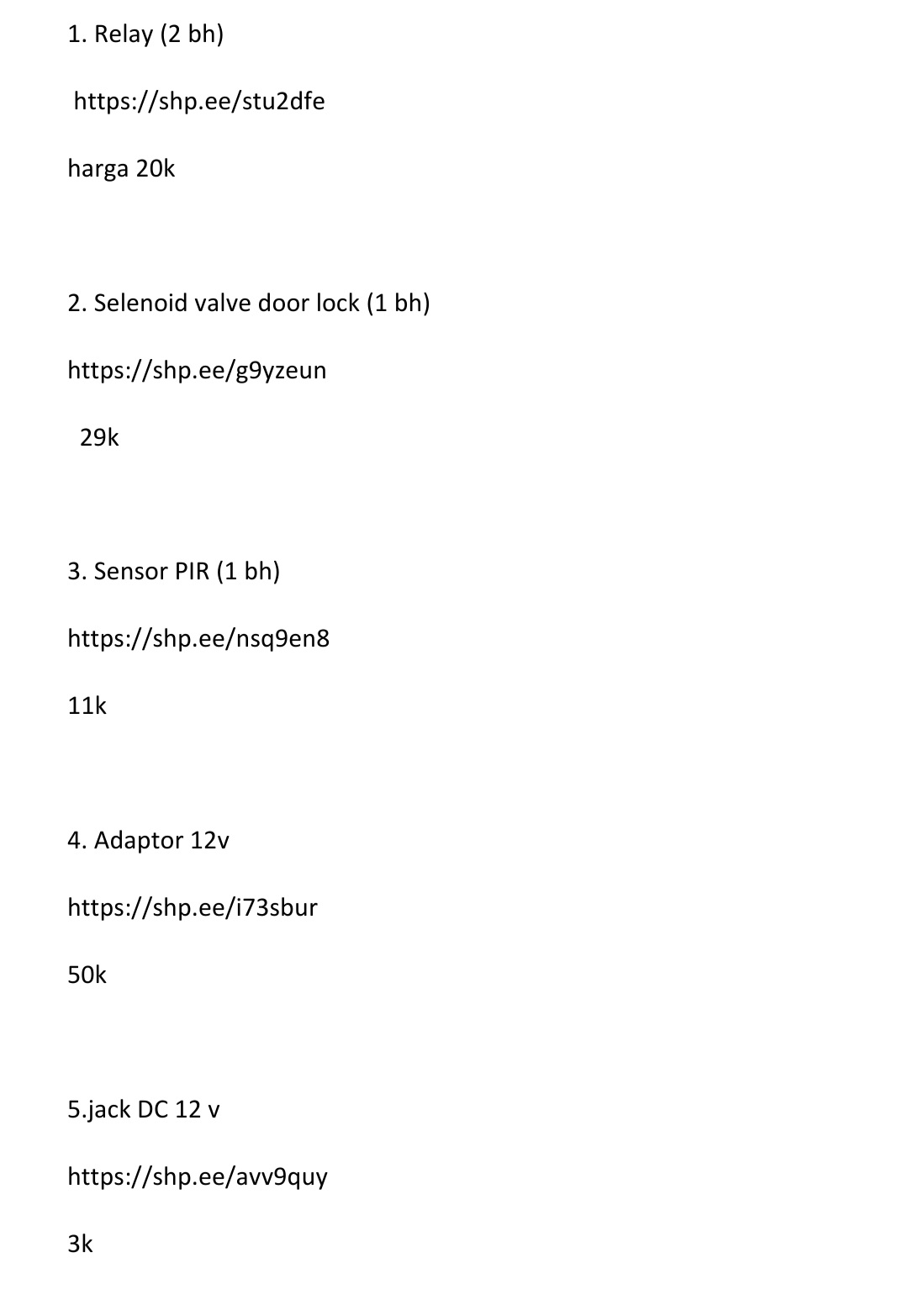
Electronic Schematic



Hardware Design



BOM



Process Flow

